Dear Students,

Warm greetings from VETBVL Polytechnic.

We are happy to inform you that

'Tessolve semiconductor Pvt.Ltd', is interested to recruit diploma holders of ECE - 2020 batch.

Below is the job description for your reference;

Designation: Test Engineer

Eligibility: Diploma (EEE & ECE)

Year of Passing: 2020

Percentage/CGPA: 7 CGPA and above

Location: Bangalore

Job Type: Full Time

Bond: 3 years Compulsory

CTC: As per company standards

Roles and Responsibilities:

· ATE based testing (Verigy/Advantest, Teradyne/Eagle, any other testers including custom made testers)

· Bench testing – TestStand, LabView tools.

· Hardware design engineer - Board design (Digital, Mixed signal, RF).

· Application engineer in ATE.

· FPGA testing using JTAG/Boundary Scan.

· System level testing, Embedded testing & debugging.

· Data Analysis and report generation.

• Good in basics of Electrical and Electronics Concepts, Good communication skills.

Skills:

- Should have good analytical and problem solving skills, ability to express new ideas, inter personal skills.

If you are interested contact me at my mobile +91 99454 60114 for further details.